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APPLICANT
N. MASUDA et al.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

WV		V. von Kaenel et al., "A 600MHz CMOS PLL Microprocessor Clock Generator with a 1.2GHz VCO", <u>1998 IEEE International Solid-State Circuits Conference Digest of Technical Papers</u> , April 1998, pp. 396-397, ISBN 0-7803-4344-1 (paper presented on February 7, 1998).

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